

1. Explain the following terms in detail: (60%)

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|-----------------------|----------------|
| (a) critical path     | (b) setup time |
| (c) power dissipation | (d) clock skew |
| (e) RTL               | (f) hard IP    |

2. Describe the difference between full custom and Cell-based design flow. (15%)

3. What is the difference between “a front-end designer” and  
“a back-end designer” ? (10%)

4. Explain the difference of the following two graphic symbols (5%) and draw their timing outputs Q and F (gate delay is ignored) for the input sequence (10%).

本題答案寫在題目卷並交回

