

Call For Paper
2010 Workshop of
Computer Architecture, SoC, and Embedded Systems
(In conjunction with ICS 2010)
December 16-18, 2010
National Cheng Kung University, Tainan City, Taiwan (R.O.C.)
<http://ics-2010.csie.ncku.edu.tw/>

The 2010 International Workshop on Computer Architecture, SoC, and Embedded Systems intends to provide a forum for researchers, educators, and professionals to exchange their discoveries and practices, and to explore future trends and applications in Computer Architecture, SoC, and Embedded Systems. Your presentation at the workshop would be an excellent opportunity for you to meet colleagues and to discuss technology advancements.

Possible topics of interest include, but are not limited to

- Processor architectures
- Multi-core systems, architectures, and virtual platforms
- Memory hierarchy subsystems
- I/O and storage subsystems
- Performance/power evaluation and/or measurement of real systems
- Compiler and architecture interaction
- Power-efficient designs
- Application-specific, reconfigurable, and embedded VLSI architectures
- Hardware/software co-design
- ESL simulations of SoC systems or embedded systems
- All aspects of test and verification in processors or SoC systems

Paper Submission

Authors are invited to submit original papers which have not published elsewhere or have submitted in parallel to any other conferences for reviews. Paper must be prepared in PDF form. Submissions must not exceed 6 pages, including tables and figures. Once the paper is accepted, at least one of the authors will register and present the paper in the conference. For details, please visit the conference website at <http://ics-2010.csie.ncku.edu.tw/>.

Formatting Instructions:

8.5" x 11" x 2 (DOC, PDF)

<http://ics-2010.csie.ncku.edu.tw/index.php?page=cfp>

Important Dates

Paper Submission Due: Aug. 1st, 2010

Acceptance Notification: Sep. 15th, 2010

Final Camera-Ready Due: Oct. 10th, 2010

Conference Dates: December 16-18, 2010

Chair

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